

FIG. 1

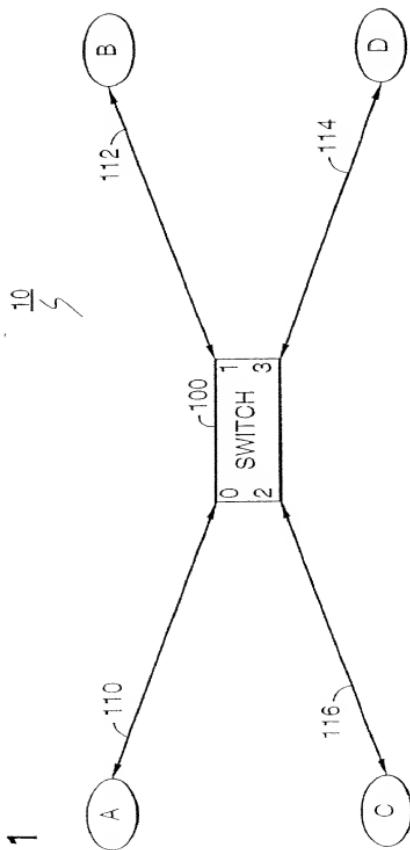


FIG. 3A

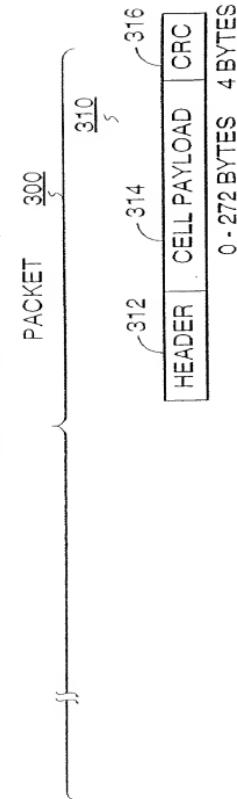
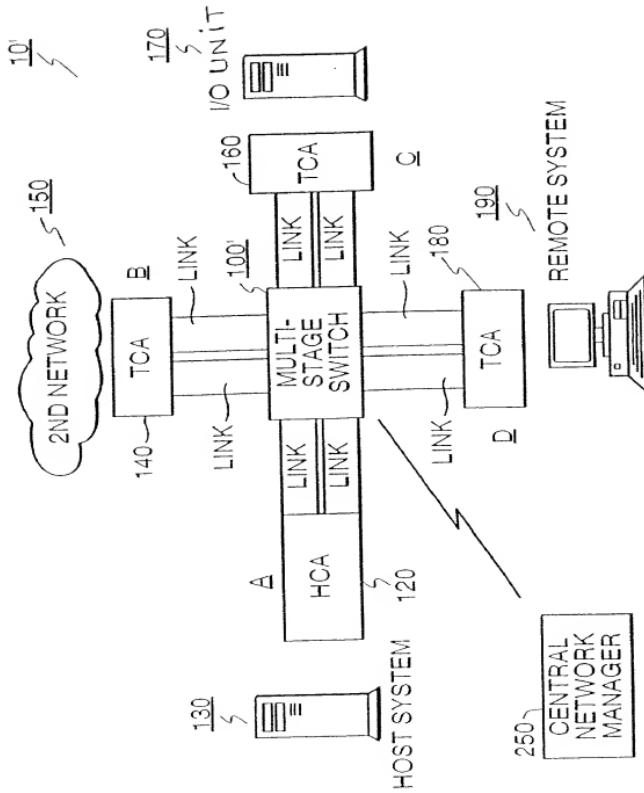


FIG. 2



350A
9
360
7

CONTROL SEGMENT		
LENGTH	MEMORY HANDLE	VIRTUAL ADDRESS
372	374	376

370

FIG. 3B

350B
9
360
7

CONTROL SEGMENT		
	REMOTE MEMORY HANDLE	REMOTE VIRTUAL ADDRESS
	382	384
LENGTH	LOCAL MEMORY HANDLE	LOCAL VIRTUAL ADDRESS
372	374	376

380
370

FIG. 3C

FIG. 4A

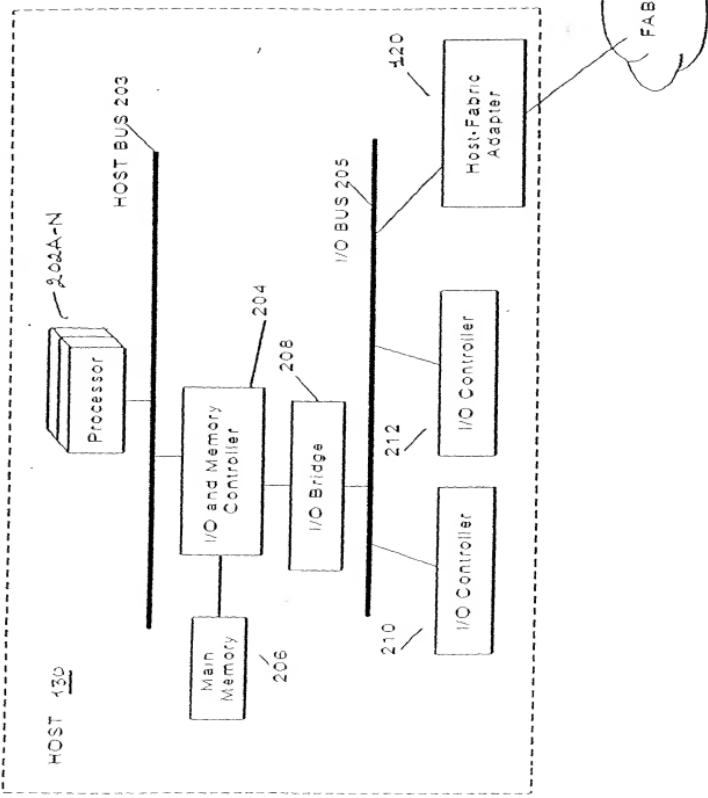
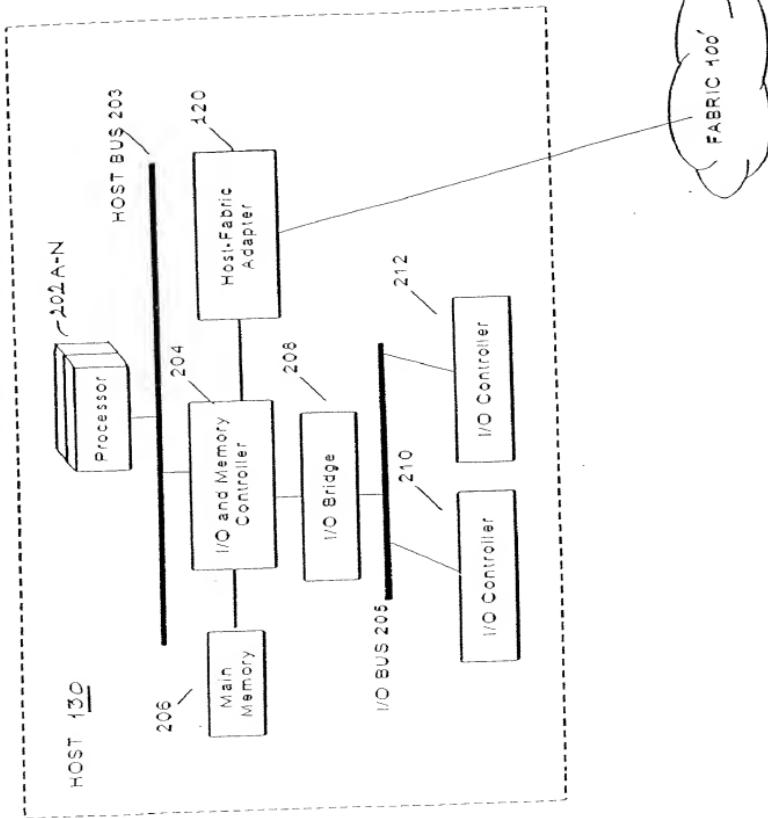
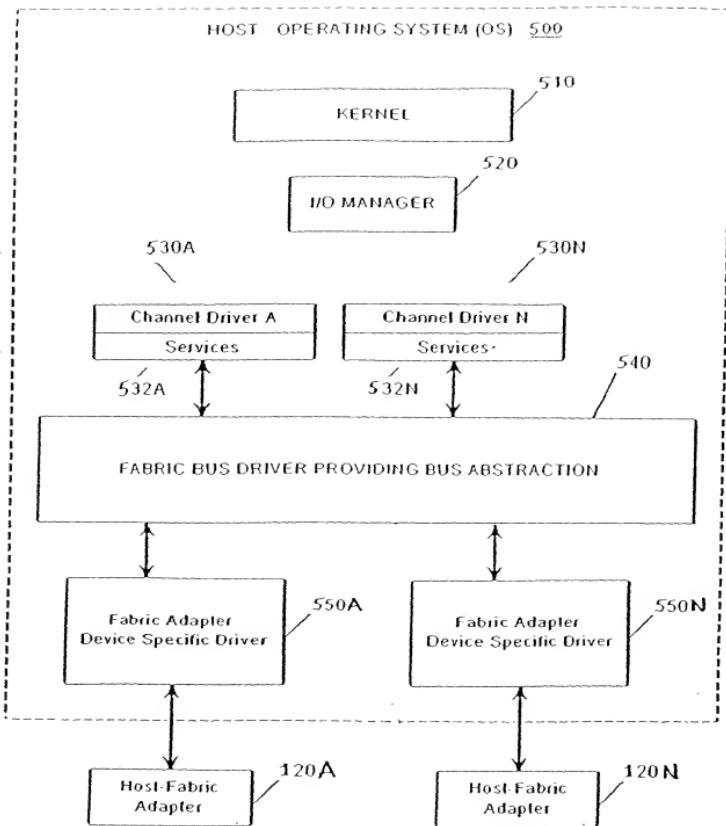


FIG. 4B





EXAMPLE SOFTWARE DRIVER STACKS OF HOST SYSTEM

FIG. 5

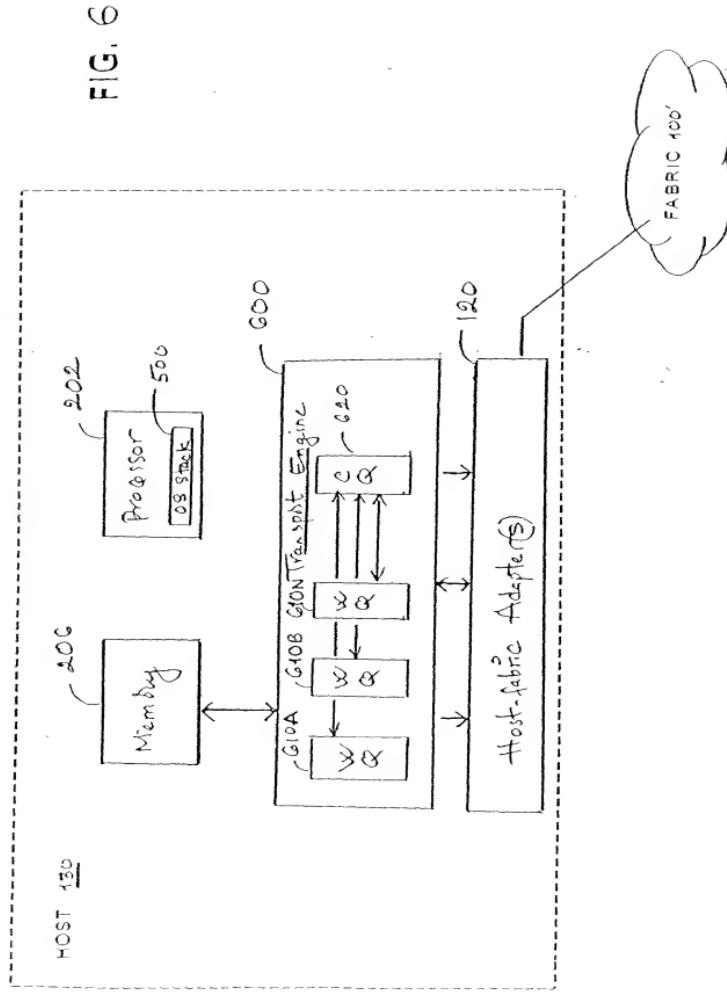


FIG. 7

120

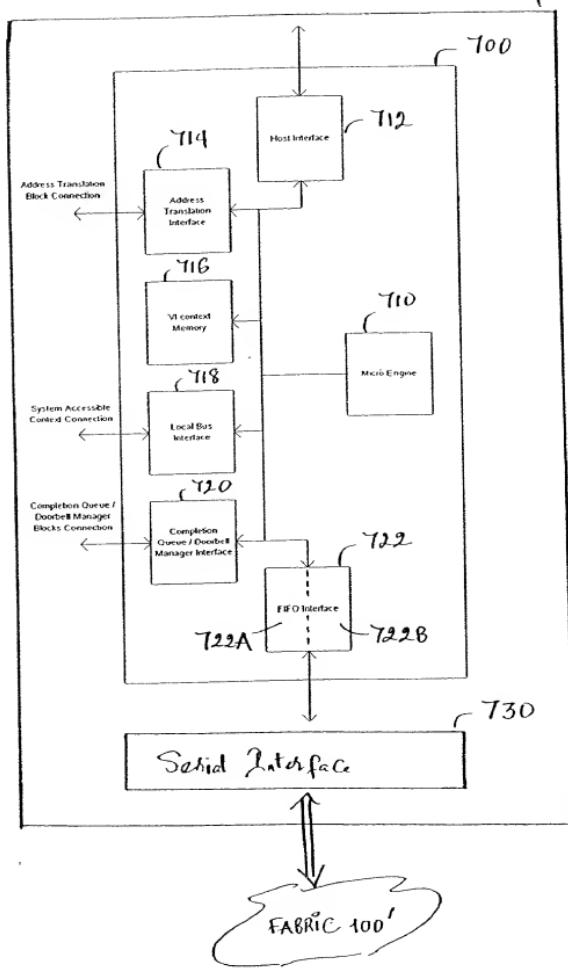


FIG. 8

110
3

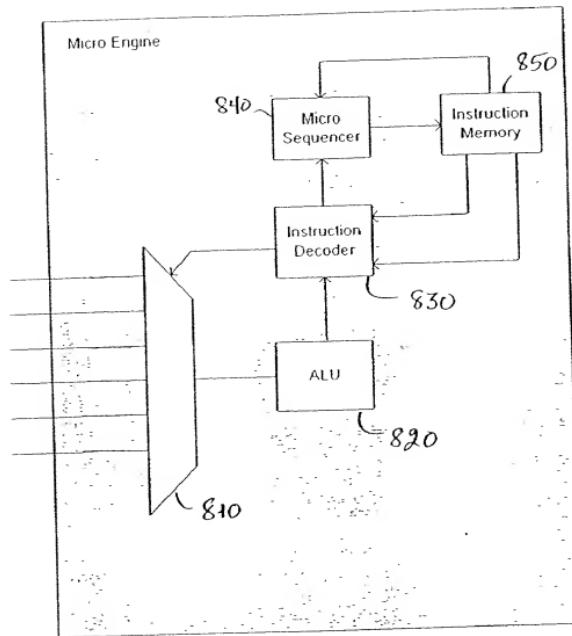


FIG. 9

Branch Control Address Address ... (bit 21-10)

70

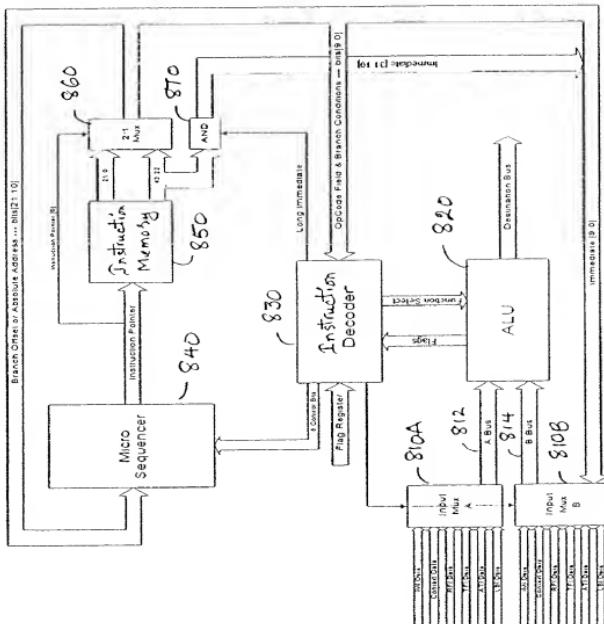
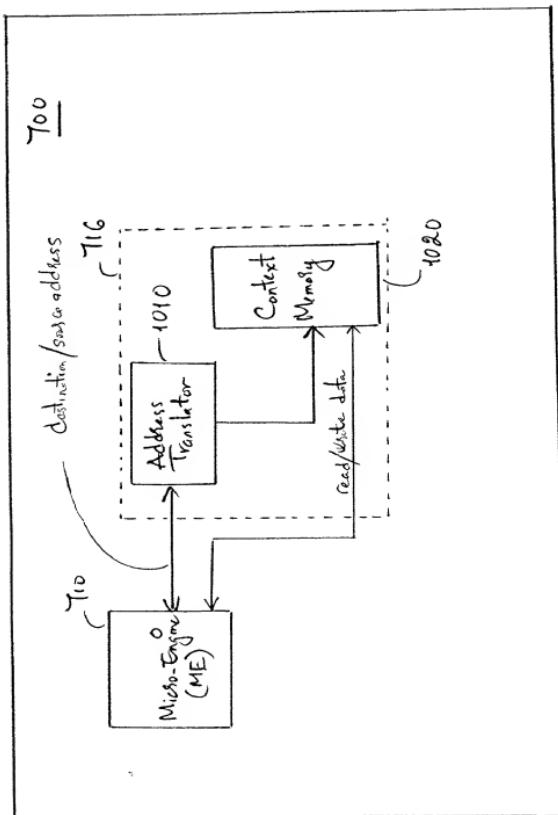


FIG. 10



Single 40x32

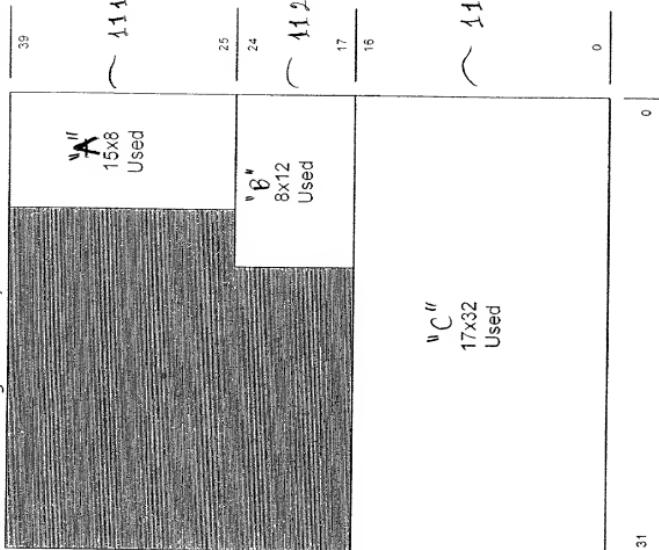
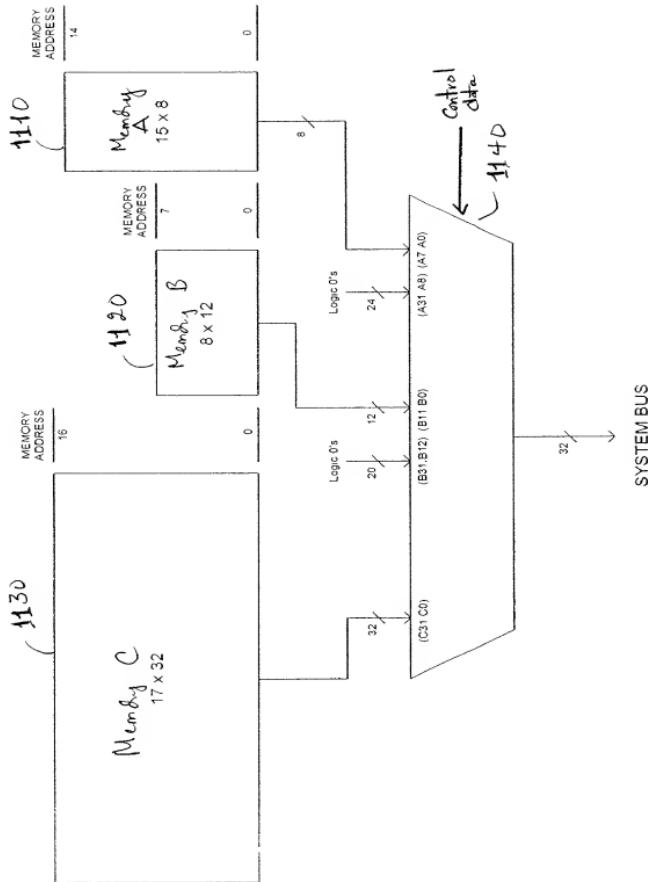
MEMORY
ADDRESS

FIG. 11A



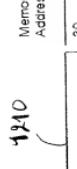


FIG. 12A

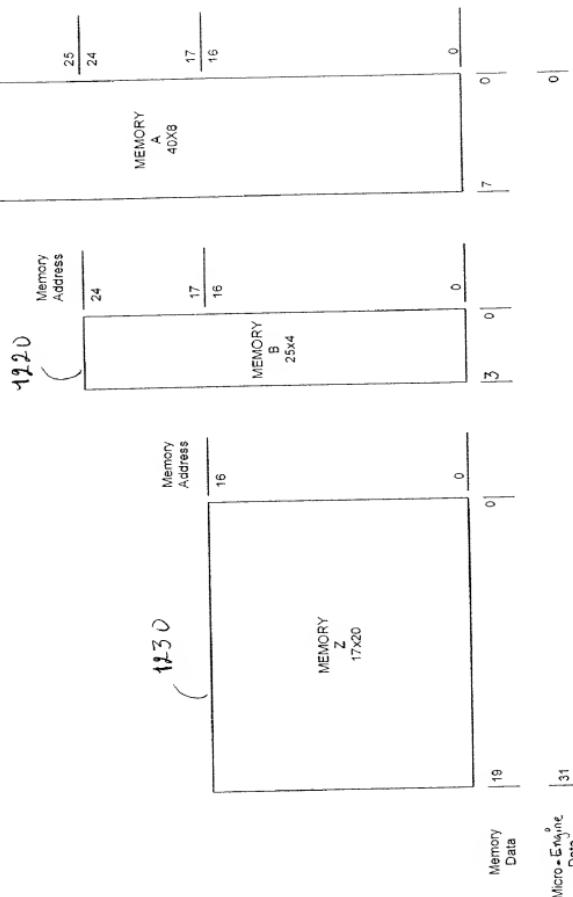
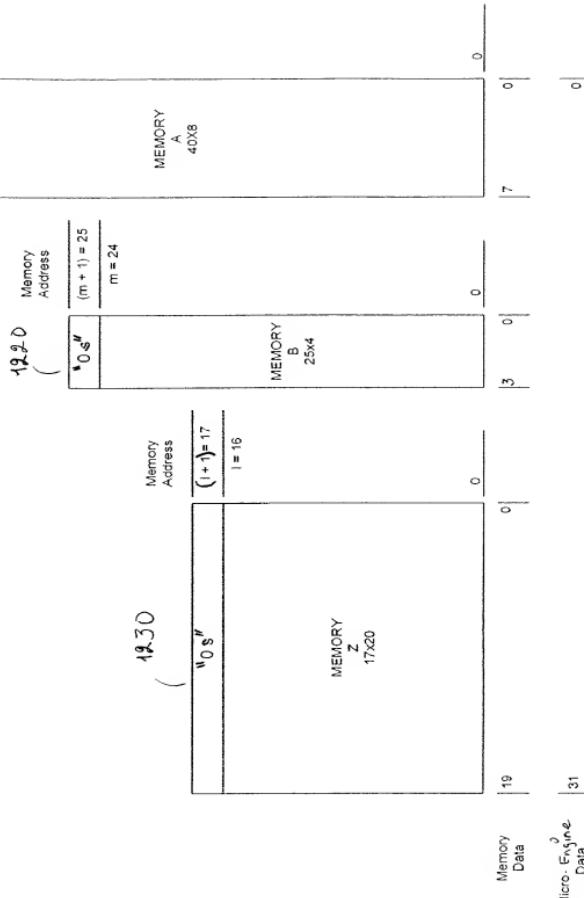


FIG. 19 B



1310

49

FIG. 13A

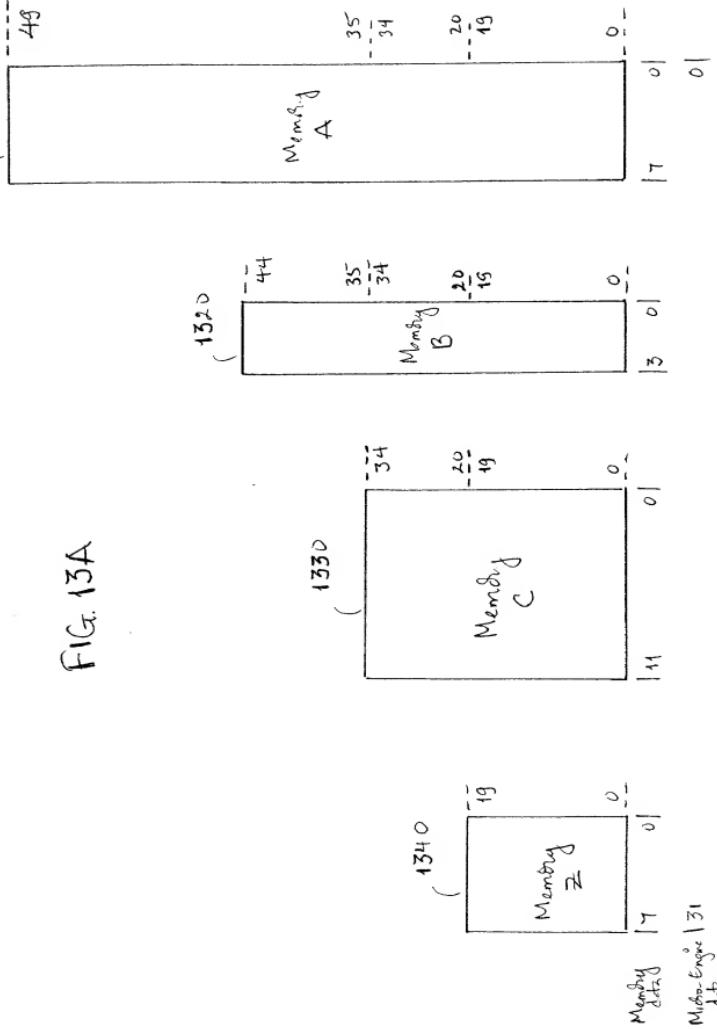
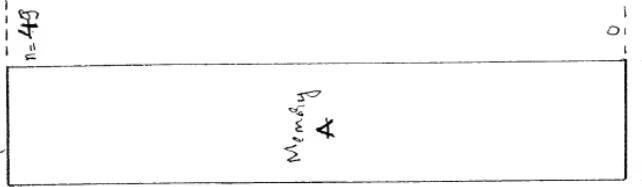


FIG. 13B

Memory cell 1310

1310

$n=49$



1320

$$\left(\begin{array}{c} n=48 \\ \hline l+1 \end{array} \right) = 45$$

Memory A

Memory B

$$\left(\begin{array}{c} l+1 \\ \hline 34 \end{array} \right) = 35$$

1330

1330

$$\left(\begin{array}{c} n=48 \\ \hline l+1 \end{array} \right) = 35$$

Memory C

$$\left(\begin{array}{c} n=48 \\ \hline l+1 \end{array} \right) = 35$$

$$\left(\begin{array}{c} n=48 \\ \hline l+1 \end{array} \right) = 45$$

Memory
d₁₄

01

1340

$$\left(\begin{array}{c} n=48 \\ \hline l+1 \end{array} \right) = 49$$

Memory Z

$$\left(\begin{array}{c} n=48 \\ \hline l+1 \end{array} \right) = 49$$

$$\left(\begin{array}{c} n=48 \\ \hline l+1 \end{array} \right) = 45$$

Memory
d₁₄

Memory
d₁₄

1410

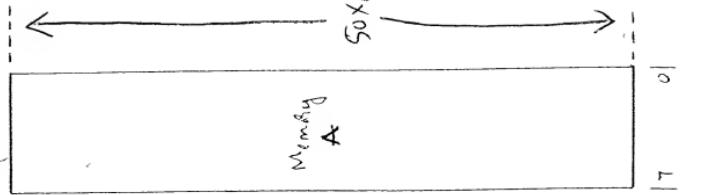
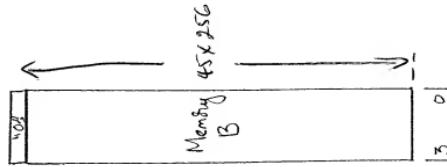
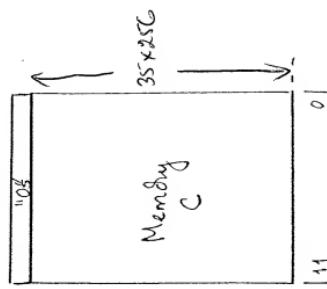


FIG. 14

1420



1430



1440

